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APPLICANTS

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** CONTINUING DATA ***** *BT*

** FOREIGN APPLICATIONS ***** *BT*
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IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>[Signature]</i> Examiner's Signature	STATE OR COUNTRY AUSTRIA	SHEETS DRAWING 2	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 2
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TITLE

Method and device for testing the mapping/implementation of a model of a logic circuit onto/in a hardware emulator

	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing)
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